



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jinghui Lu et al.
Assignee: Xilinx, Inc.
Title: "AN INTEGRATED HIGH-SPEED SERIAL-TO-PARALLEL AND PARALLEL-TO-SERIAL TRANSCEIVER"

Serial No.: 10/051,222 Filing Date: January 18, 2002
Examiner: Juan A. Torres Art Unit: 2631
Docket No.: X-1055 US Conf. No.: 7174

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
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Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

Dear Examiner:

In response to the first Office Action mailed from the Patent Office on July 1, 2005, please amend the indicated beginning on the following page.

Amendments to the Drawings begin on page 2 of this paper and include both a replacement sheet and an annotated sheet showing the changes.

Amendments to the Claims begin on page 3 of this paper.

Remarks begin on page 13 of this paper.